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Raghavan, G.; Jensen, J.F.; Walden, R.H.; Posey, W.P.;

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC 1997 IEEE International, 6-8 Feb. 1997

Pages:214 - 215, 459

[\[Abstract\]](#) [\[PDF Full-Text \(1436 KB\)\]](#) **IEEE CNF**
2 Narrow-band variable center frequency single-loop and multistage sigma-delta modulators for bandpass signals

Al-Janabi, M.; Kale, I.; Morling, R.C.S.;

Instrumentation and Measurement, IEEE Transactions on, Volume: 48, Issue 5, Oct. 1999

Pages:873 - 877

[\[Abstract\]](#) [\[PDF Full-Text \(120 KB\)\]](#) **IEEE JNL**
3 InP HBT continuous time 2nd order bandpass $\Delta\Sigma$ modulator and 1-to DEMUX IC with center frequency continuously programmable from 0 to 100 MHz

Jensen, J.F.; Raghavan, G.; Stanchina, W.; Walden, R.H.; Hafizi, M.; Sun, H.C.; Kardos, M.; Brown, Y.K.; Montes, M.E.;

Indium Phosphide and Related Materials, 1997., International Conference on, 15 May 1997

Pages:237 - 240

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) **IEEE CNF**
4 Low-voltage high-speed switched-capacitor circuits without voltage

bootstrapper

Lei Wang; Embabi, S.H.K.;

Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 8 , Aug. 2003

Pages:1411 - 1415

[\[Abstract\]](#) [\[PDF Full-Text \(437 KB\)\]](#) IEEE JNL

5 Superconducting bandpass $\Delta\Sigma$ modulator with 2.23 GHz center frequency and 42.6 GHz sampling rate

Bulzacchelli, J.F.; Hae-Seung Lee; Misewich, J.A.; Ketchen, M.B.;

Solid-State Circuits Conference, 2002. Digest of Technical Papers. ISSCC. 200 IEEE International , Volume: 1 , 3-7 Feb. 2002

Pages:188 - 459 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(387 KB\)\]](#) IEEE CNF

6 A new SC differentiator for two-path bandpass /spl Delta//spl Sigm modulator design

Lei Wang; Embabi, S.H.K.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactio [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volu 49 , Issue: 11 , Nov. 2002

Pages:728 - 732

[\[Abstract\]](#) [\[PDF Full-Text \(337 KB\)\]](#) IEEE JNL

7 Superconducting bandpass /spl Delta//spl Sigma/ modulator with ; GHz center frequency and 42.6-GHz sampling rate

Bulzacchelli, J.F.; Hae-Seung Lee; Misewich, J.A.; Ketchen, M.B.;

Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 12 , Dec. 2002

Pages:1695 - 1702

[\[Abstract\]](#) [\[PDF Full-Text \(527 KB\)\]](#) IEEE JNL

8 An 81-MHz IF receiver in CMOS

Hairapetian, A.;

Solid-State Circuits, IEEE Journal of , Volume: 31 , Issue: 12 , Dec. 1996

Pages:1981 - 1986

[\[Abstract\]](#) [\[PDF Full-Text \(752 KB\)\]](#) IEEE JNL

9 A fourth-order bandpass delta-sigma modulator with reduced numb of op amps

Bang-Sup Song;

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 12 , Dec. 1995

Pages:1309 - 1315

[\[Abstract\]](#) [\[PDF Full-Text \(748 KB\)\]](#) IEEE JNL

10 Bandpass $\Delta\Sigma$ modulator with frequency converters in feedback loo

Yasuda, A.; Tanimoto, H.;

Electronics Letters , Volume: 34 , Issue: 24 , 26 Nov. 1998

Pages:2293 - 2295

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **IEEE JNL**

11 A two-path bandpass $\Sigma\Delta$ modulator for digital IF extraction at 20 M
Ong, A.K.; Wooley, B.A.;
Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC
1997 IEEE International , 6-8 Feb. 1997
Pages:212 - 213, 459

[\[Abstract\]](#) [\[PDF Full-Text \(1408 KB\)\]](#) **IEEE CNF**

12 Multibit bandpass delta-sigma modulators using N-path structures
Schreier, R.; Temes, G.C.; Yesilyurt, A.G.; Zhang, Z.X.; Czarnul, Z.; Hairapet.
A.;
Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International
Symposium on , Volume: 2 , 3-6 May 1992
Pages:593 - 596 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) **IEEE CNF**

13 New multirate bandpass sigma-delta modulators
Colodro, F.; Torralba, A.; Vega-Leal, A.P.; Ridao, F.P.;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuit
Systems I: Fundamental Theory and Applications, IEEE Transactions on] , Vol
51 , Issue: 11 , Nov. 2004
Pages:2141 - 2147

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) **IEEE JNL**

14 High dynamic range InP HBT delta-sigma analog-to-digital convert
Inerfield, M.; Skones, W.; Nelson, S.; Ching, D.; Cheng, P.; Wong, C.;
Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 9 , Sept. 2003
Pages:1524 - 1532

[\[Abstract\]](#) [\[PDF Full-Text \(682 KB\)\]](#) **IEEE JNL**

**15 A 2.2-mW CMOS bandpass continuous-time multibit /spl Delta/-/spl
Sigma/ ADC with 68 dB of dynamic range and 1-MHz bandwidth for
wireless applications**
Kappes, M.S.;
Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 7 , July 2003
Pages:1098 - 1104

[\[Abstract\]](#) [\[PDF Full-Text \(531 KB\)\]](#) **IEEE JNL**

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Applied Superconductivity, IEEE Transactions on , Volume: 13 , Issue: 2 , Jun 2003

Pages:471 - 476

[\[Abstract\]](#)[\[PDF Full-Text \(423 KB\)\]](#)**IEEE JNL****17 A 80-MHz bandpass ΔΣ modulator for a 100-MHz IF receiver***Salo, T.; Lindfors, S.; Halonen, K.A.I.;*

Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 7 , July 2002

Pages:798 - 808

[\[Abstract\]](#)[\[PDF Full-Text \(393 KB\)\]](#)**IEEE JNL****18 A double-sampling pseudo-two-path bandpass ΔΣ modulator***Shen-Iuan Liu; Chien-Hung Kuo; Ruey-Yuan Tsai; Jingshown Wu;*

Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 2 , Feb. 2000

Pages:276 - 280

[\[Abstract\]](#)[\[PDF Full-Text \(124 KB\)\]](#)**IEEE JNL****19 Bandpass ΣΔ modulator employing undersampling of RF signals for wireless communication***Hussein, A.I.; Kuhn, W.B.;*

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactio [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Vol 47 , Issue: 7 , July 2000

Pages:614 - 620

[\[Abstract\]](#) [\[PDF Full-Text \(176 KB\)\]](#) [IEEE JNL](#)

20 **A 160-MHz fourth-order double-sampled SC bandpass sigma-delta modulator**

Bazarjani, S.; Snelgrove, W.M.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactio
[see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Vol
45 , Issue: 5 , May 1998

Pages:547 - 555

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) [IEEE JNL](#)

21 **A two-path bandpass $\Sigma\Delta$ modulator for digital IF extraction at 20 M**

Ong, A.K.; Wooley, B.A.;

Solid-State Circuits, IEEE Journal of , Volume: 32 , Issue: 12 , Dec. 1997

Pages:1920 - 1934

[\[Abstract\]](#) [\[PDF Full-Text \(336 KB\)\]](#) [IEEE JNL](#)

22 **A 40 MHz IF fourth-order double-sampled SC bandpass $\Sigma\Delta$ modulat**

Bazarjani, S.; Snelgrove, M.;

Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE Internatio
Symposium on , Volume: 1 , 9-12 June 1997

Pages:73 - 76 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) [IEEE CNF](#)

23 **A 320-MHz four-paths bandpass sigma-delta modulator**

Centuori, A.; Gatti, U.; Malcovati, P.; Maloberti, F.;

Instrumentation and Measurement Technology Conference, 2002. IMTC/2002.
Proceedings of the 19th IEEE , Volume: 1 , 21-23 May 2002

Pages:497 - 500 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(386 KB\)\]](#) [IEEE CNF](#)

24 **Chip design of a bandpass sigma-delta modulator**

Sau-Mou Wu; Rou-Yi Liu; Wei Wu; Che-Pin Chen;

Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE Internatio
Conference on , Volume: 3 , 2-5 Sept. 2001

Pages:1375 - 1378 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(224 KB\)\]](#) [IEEE CNF](#)

25 **A 15 b 30 kHz bandpass sigma-delta modulator**

Longo, L.; Horng, B.-R.;

Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC
1993 IEEE International , 24-26 Feb. 1993

Pages:226 - 227, 293

[\[Abstract\]](#) [\[PDF Full-Text \(636 KB\)\]](#) [IEEE CNF](#)

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BRS	S3	4	S1 and (rtz or rz) near2 dac	US-PGPUB; USPAT	16-Mar-05
BRS	S4	5	S1 and (band adj1 pass adj1 filter or bpf) near5 center near5 frequency	US-PGPUB; USPAT	16-Mar-05
BRS	S5	1492	341/143.ccls.	US-PGPUB; USPAT	17-Mar-05
BRS	S6	1687	341/143.ccls.	US-PGPUB; USPAT; EPO; JPO	17-Mar-05
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BRS	S11	10	S10 and bandpass	US-PGPUB; USPAT; EPO; JPO	17-Mar-05
BRS	S12	3	S9 and (return adj2 zero or rz or rtz) and (center near2 frequency)	US-PGPUB; USPAT; EPO; JPO	17-Mar-05